



# Influence of post-deposition annealing on the transport properties of sputtered Bi<sub>2</sub>Se<sub>3</sub> thin films

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## ARTICLE INFO

### Keywords:

Topological Insulators  
Thin films  
Magnetron Sputtering  
Spintronics

## ABSTRACT

The fabrication of chalcogenide-based topological insulator (TI) thin films with low defects and vacancies is an important research problem. In the past decade, Bi<sub>2</sub>Se<sub>3</sub> is one of the most investigated TI materials. The physical properties of Bi<sub>2</sub>Se<sub>3</sub> are dictated by selenium vacancies irrespective of the growth method. Controlling the number of vacancies is very challenging, but it can be influenced by substrate and growth temperature. In this work, we demonstrate that post-deposition annealing temperature is also effective in controlling the carrier concentration of Bi<sub>2</sub>Se<sub>3</sub> thin films. Ultra-thin Bi<sub>2</sub>Se<sub>3</sub> films were fabricated on quartz substrates using magnetron sputtering at room temperature, and their resistivity, bulk carrier concentration, and bulk mobility are measured as a function of the post-deposition annealing temperature under vacuum conditions ranging from 180–350 °C. We find that the carrier concentration can vary by an order of magnitude, and we obtained values as low as  $\sim 1 \times 10^{19} \text{ cm}^{-3}$  between 200–250 °C which compares very well with some of the best literature reports on films made by molecular beam epitaxy. Room temperature bulk mobility values in excess of 100 cm<sup>2</sup>/Vs were recorded for the optimally annealed samples. Energy dispersive x-ray spectroscopy measurements showed that selenium-rich Bi<sub>2</sub>Se<sub>3</sub> films have the highest carrier concentration values. X-ray diffraction measurements showed that the best crystallographic properties are obtained for stoichiometric Bi<sub>2</sub>Se<sub>3</sub>. Our results indicate that higher mobility values may be possible if better crystal structure and lower resistivity are obtained in selenium-rich films.

## 1. Introduction

The discovery of a number of Bismuth-based 3D topological insulators (TI) at room temperature [1–5] has generated significant interest in areas such as spintronics [6–10], semiconductors [11], plasmonics [12,13], Terahertz photodetection [14,15] and fault-tolerant quantum computing [16–18]. The most promising TI materials are low band gap chalcogenides such as Bi<sub>2</sub>Se<sub>3</sub> and Bi<sub>2</sub>Te<sub>3</sub>. Despite the confirmation of the protected surface-states through photoemission measurements [4,3,5], device applications rely on electronically isolating the surface states from the overwhelmingly large concentration of bulk carriers. Therefore, there is significant interest to grow high quality TI thin-films using methods such as molecular beam epitaxy (MBE) [19–27], chemical vapour deposition (CVD) [28,29], pulsed laser deposition (PLD) [30–34], and magnetron sputtering [35–37].

In an MBE process, the individual elements are co-evaporated in an ultra-high vacuum environment. This leads to a highly controlled and epitaxial growth of TI thin films [38–41]. Compared to MBE, PLD and

magnetron sputtering are more aggressive and typically employ a process gas such as Argon to fabricate TI thin films [30]. However, it can be argued that both MBE and PLD may be too expensive for device applications when compared to CVD and magnetron sputtering [41,42]. CVD is not a preferred method for growing heterostructures since it may lead to contamination during the sample transfer process [41]. Growth of TI thin films using magnetron sputtering is receiving significant attention, particularly for spintronics applications. Recent research on Bi<sub>2</sub>Se<sub>3</sub>-CoFeB bilayers, grown entirely using magnetron sputtering, demonstrated higher spin-orbit torque on the ferromagnetic CoFeB layer compared to MBE-grown Bi<sub>2</sub>Se<sub>3</sub> [7]. Such studies highlight that magnetron sputtering may be more suitable for certain applications.

Transport properties of chalcogenide-based TI materials such as Bi<sub>2</sub>Se<sub>3</sub> are dominated by unintentional *n*-type doping due to the presence of native defects such as chalcogen (Se) vacancies or antisites [43], irrespective of the growth conditions (e.g., Bi-rich or Se-rich) [44] or method (single crystal, thin films) [45,34]. Selenium vacancies have been mapped using scanning tunneling microscopy in Bi<sub>2</sub>Se<sub>3</sub> films [46] and cleaved crystals [44]. In thin films, carrier concentrations are

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<https://doi.org/10.1016/j.tsf.2021.138676>

Received 7 July 2020; Received in revised form 18 March 2021; Accepted 1 April 2021

Available online 5 April 2021

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typically in the  $\sim 10^{19}$ - $10^{20}$   $\text{cm}^{-3}$  range and values below  $10^{18}$   $\text{cm}^{-3}$  are difficult to obtain. The lowest bulk carrier concentration ( $\sim 10^{16}$  -  $10^{17}$   $\text{cm}^{-3}$ ) are reported on single crystals [45,47]. A theoretical study has indicated that a carrier concentration of  $1 \times 10^{15}$   $\text{cm}^{-3}$  may be possible [43].

Transport properties of thin films are also controlled by the film-substrate interface. Typically, the resistivity (mobility) increases (decreases) significantly at the ultra thin-film level due to higher scattering at the film-substrate interface [34,48]. Several studies also show that carrier concentration also varies significantly at the ultra-thin film level [48,49]. The best quality 5–10 nm  $\text{Bi}_2\text{Se}_3$  thin films grown on standard substrates ( $\text{Al}_2\text{O}_3$ ,  $\text{Si}/\text{SiO}_2$ ,  $\text{Si}$ ) show carrier concentration values in the  $(0.5\text{-}2) \times 10^{19}$   $\text{cm}^{-3}$  range [8,19,22,32,50–52]. Koirala *et al.* [49] demonstrated that carrier concentration can be reduced by up to an order of magnitude by employing suitable substrate and interface engineering that also resulted in ultra-high surface mobility of up to 16000  $\text{cm}^2/\text{Vs}$  at 2 K. Such values already approach the best single crystal results [47]. Other strategies to lower carrier concentration values involve substitutional doping of Bi with Sb [53].

Reports on  $\text{Bi}_2\text{Se}_3$  films grown with magnetron sputtering are relatively sparse. Some studies on magnetron sputtered  $\text{Bi}_2\text{Se}_3$  films highlighted the role of substrate temperature on the Bi:Se ratio [37,36,54,55]. While high temperature is generally preferred for epitaxial growth, it typically results in high concentration of selenium vacancies [36,37]. Therefore, some groups combined room or high temperature growth with subsequent Selenium annealing [55,56]. In our previous work, we established and characterized the optical [57] and transport [58] properties of 2-100 QL  $\text{Bi}_2\text{Se}_3$  grown at room temperature and annealed under high vacuum for 2 h at 300 °C. The conditions were optimized to promote the highest x-ray diffraction intensity and reaffirmed in this work. Despite high crystalline quality, transport measurements [58] indicated that the properties were significantly inferior compared to MBE grown films as evidenced through high carrier concentration ( $\sim 10^{20}$   $\text{cm}^{-3}$ ) and a low Hall mobility of  $\sim 25$   $\text{cm}^2/\text{V.s}$  for a 6 QL sample grown on amorphous quartz. This provided the motivation to correlate the growth-structure-property relationships in greater detail and test the limits of the sputtering process.

In this work, we show that the transport properties can be substantially improved by adjusting the post-deposition vacuum annealing conditions. We observed low carrier concentration ( $\sim 1 \times 10^{19}$   $\text{cm}^{-3}$ ) and significantly higher mobility values (112  $\text{cm}^2/\text{V.s}$ ) in ultra-thin samples annealed between 220-250 °C for 30 min. This improvement is significant compared to our previous work [58] and compares very well with the best values reported using MBE. Low carrier concentration and high mobility values are observed in selenium rich samples, whereas the best crystal quality is observed in stoichiometric (Bi:Se = 2:3) samples when annealed at 300 °C. Overall, we show that our sputter deposition method can produce high quality TI thin films with superior transport properties and infer that the method can be extended to other TI materials.

## 2. Experimental details

Ultrathin  $\text{Bi}_2\text{Se}_3$  thin films of nominal 7 nm thickness were rf sputtered onto  $10 \times 10$   $\text{mm}^2$  amorphous quartz substrate from a stoichiometric target (Kurt Lesker, 99.999% purity) at 6.66 Pa Ar gas pressure in a high vacuum chamber (base pressure  $5.3 \times 10^{-7}$  Pa). Samples were sputtered at room temperature and subsequently annealed *in-situ* for 30 min at various temperatures between 180-350 °C. Thickness, roughness, and density of films were obtained using the X-ray reflectivity (XRR) method and crystal structure was determined by employing the X-ray diffraction (XRD) method. Both XRR and XRD measurements were performed using a Rikagu Smartlab Diffractometer with Cu-K $\alpha$  radiation. The elemental composition was examined using energy dispersive X-ray (EDX) spectroscopy at an accelerating 20 kV beam spot of 3-3.5 mm equipped with FEI Quanta EFG 450SEM. Longitudinal resistivity ( $\rho$ )

and Hall measurements were performed at 0.7 Tesla using a Nano-Magnetics Instruments ezHEMS system in the Van-der-Pauw (VDP) geometry to extract the bulk carrier concentration ( $n_{3D} = 1/(eR_H)$ , where  $R_H$  is the Hall coefficient and  $e$  is the elementary charge), and Hall mobility ( $\mu = R_H/\rho$ ). The surface carrier concentration is extracted from the bulk concentration,  $n_{2D} = n_{3D}t$ , where  $t$  is the thickness of film. The four-probe contact was made using Au/Cr tips on silver paint placed at the corners of square samples. This provided a linear Current-Voltage curve.

## 3. Results and discussions

In Fig. 1a, we show the representative XRR data of a  $\sim 7$  nm  $\text{Bi}_2\text{Se}_3$  thin film annealed at 180 °C. Pronounced oscillations patterns are revealed with sharp interfaces, and a surface roughness value of less than 0.5 nm was obtained after data fitting. Ultra-smooth and uniform samples were obtained under other annealing conditions. Also, we fitted the X-ray reflectivity data with an oxide layer and did not find any noticeable improvement to the fit. This finding is similar to our earlier conclusion from optical spectroscopy [57]. Taking all the data into consideration, the surface oxidation effect will be neglected throughout the rest of our discussion.

In Fig. 1b, we show the XRD pattern of all films annealed between 180–350 °C. No XRD pattern was observed for the room-temperature sample indicating that the as-grown films are amorphous in nature. Measureable XRD intensity is observed when annealed above 150 °C. The XRD patterns only show (003) family of peaks which confirms that all films are highly textured and oriented along the trigonal axis which is associated with  $\text{Bi}_2\text{Se}_3$ . We plot the intensity of the (006) diffraction peak in the inset figure to highlight the annealing temperature dependence. As observed, the intensity increased by a factor of 4 as the temperature is increased from 180-300 °C. The intensity is a maximum at 300 °C and fell sharply at 350 °C. The reduced intensity at 350 °C is due to a combination of polycrystalline growth and higher defects and vacancies. Prolonged annealing at 350 °C (about 2 h) led to chemical decomposition to Selenium deficient phases (data not shown). We thereby conclude that the best crystal structure is obtained when samples are annealed at 300 °C, which is consistent with our previous report [58]. The annealing time is significantly reduced in this work (30 mins vs 2 h in Ref. [58]). As we show, reducing the annealing time significantly improved the transport properties.

EDX measurements revealed that the composition of the films vary strongly with annealing temperature, as summarized in Table 1. We find that films annealed at 180 °C are significantly selenium rich (Bi:Se = 1:3) even though our target composition is stoichiometric (2:3). Annealing at higher temperatures reduced the relative selenium content, and the composition is very close to 2:3 at 300 °C. The selenium-rich nature at lower annealing temperatures (180-250 °C) – and presumably in as-deposited samples – implies that the sputtering process removes Selenium from the  $\text{Bi}_2\text{Se}_3$  target at a much higher rate than Bismuth. This could be due to the low binding energy of Selenium. Therefore, post-deposition annealing, in effect, is serving two purposes. One is to promote better crystallinity, and the other is to control the selenium content. While the creation of selenium vacancies was expected with annealing, the formation of selenium rich films during deposition was unexpected. It is to be noted that the best crystal quality from XRD measurements (Fig. 1b) corresponded to the stoichiometric 2:3 ratio. Additional evidence to support that the sputtering process removes Selenium at a disproportionately high rate from the target was confirmed through a direct XRD scan of the nominal  $\text{Bi}_2\text{Se}_3$  target after prolonged usage. As shown in Fig. 2, the surface of the used sputtering target matched the XRD pattern of BiSe instead of  $\text{Bi}_2\text{Se}_3$  which points to high selenium deficiency. XRD profile matching  $\text{Bi}_2\text{Se}_3$  composition was recovered after polishing the used target (data not shown). Put together, we conclude that the sputtering process resulted in a transfer of selenium-rich Bismuth selenide thin films. This observation has

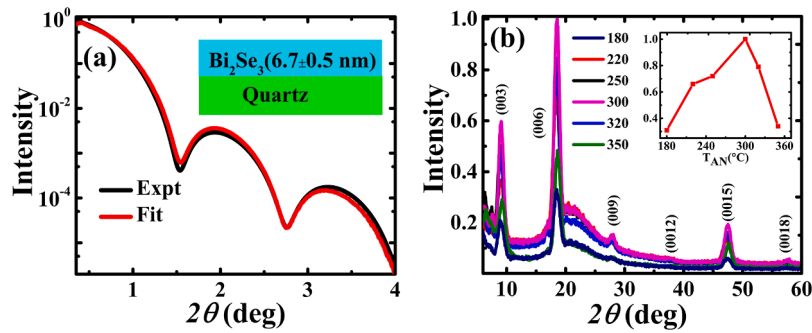


Fig. 1. (a) X-ray reflectivity data of a  $\text{Bi}_2\text{Se}_3$  sample grown on quartz substrate and annealed at 180 °C. The extracted thickness and roughness is shown in the inset. (b) Variation of X-ray diffraction pattern with annealing temperature as indicated. Inset of (b) shows variation of the (006) XRD peak with annealing temperature. Highest intensity is observed at 300 °C.

Table 1

Elemental ratio of Bismuth and Selenium vs post-deposition annealing temperature measured using Energy Dispersive X-ray spectroscopy. The carrier concentration values obtained from Hall measurements are also tabulated.

Annealing Temp. (° C)	Bismuth (Atomic %)	Selenium (Atomic %)	$n_{3D}$ ( $\text{cm}^{-3}$ )	$n_{2D}$ ( $\text{cm}^{-2}$ )
180	25	75	$1.40 \times 10^{19}$	$1.04 \times 10^{13}$
220	35	64	$1.34 \times 10^{19}$	$1.07 \times 10^{13}$
250	37	63	$2.29 \times 10^{19}$	$1.60 \times 10^{13}$
300	40	60	$3.95 \times 10^{19}$	$2.77 \times 10^{13}$
350	45	55	$1.09 \times 10^{20}$	$8.73 \times 10^{13}$

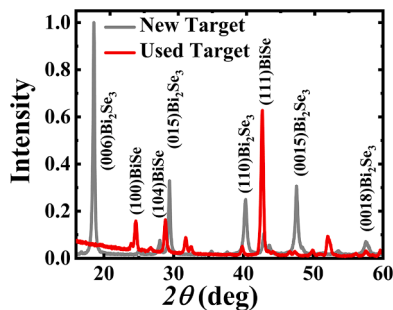


Fig. 2. Surface XRD pattern of the new (gray) and the used (red) Bismuth selenide sputtering target employed during this work. The diffraction pattern of the used target is very close to BiSe (1:1) composition, whereas the new target is  $\text{Bi}_2\text{Se}_3$  (2:3). The disproportionate removal of Selenium from the target results in Selenium-rich films.

interesting implications on the transport properties as we discuss next.

We observe that Bi:Se ratio has a strong influence on the carrier concentration as shown in Table 1 and plotted in Fig. 3. Selenium rich samples showed much lower carrier concentration. In particular, samples annealed at 180 and 220 °C show values very close to  $1 \times 10^{19} \text{ cm}^{-3}$ . These values compare very well to some of the best MBE and PLD grown samples [34,40]. Above 220 °C, the concentration increased very slowly and is  $4 \times 10^{19} \text{ cm}^{-3}$  at 300 °C. Above 300 °C, the carrier concentration is seen to increase very rapidly and is over  $10^{20} \text{ cm}^{-3}$  by 350 °C, which is similar to the values reported by other groups [35–37]. It is to be noted that the melting point of selenium (220 °C) coincides very well with the onset of the increase in the carrier concentration values. However, as noted previously, low annealing temperatures do not correspond to the best crystal structure - which is observed at 300 °C

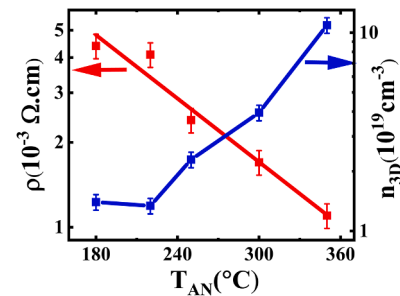


Fig. 3. Variation of room temperature resistivity ( $\rho$ ) and bulk carrier concentration ( $n_{3D}$ ) for samples treated with different post-deposition annealing temperature ( $T_{AN}$ ) as indicated.

(Fig. 1b). So we conclude that the conditions for the lowest carrier concentration and the best crystal structure do not coincide.

The bulk resistivity, on the other hand, decreased practically linearly throughout, as shown in Fig. 3, with values ranging between 4.4  $\text{m}\Omega\cdot\text{cm}$  at 180 °C to 1.1  $\text{m}\Omega\cdot\text{cm}$  at 350 °C. *N*-type behavior is observed in all films which is attributed to selenium vacancies. While resistivity can also decrease due to improved crystal structure, we infer that the decrease is primarily due to the increase in carrier concentration.

We have summarized all major transport data in Table 2 including the bulk resistivity ( $\rho$ ), the Hall coefficient ( $R_H$ ), and the Hall mobility ( $\mu$ ). We can see that the Hall coefficient has a clear maximum at 220 °C and decreased almost in the same proportion as the resistivity as the annealing temperature is increased from 220 °C to 250 °C. As a result, the Hall mobility is fairly constant in the 220–250 °C range, with values close to  $110 \text{ cm}^2/\text{V}\cdot\text{s}$ , as shown in Fig. 4a. The mobility and resistivity variation with sample temperature are plotted in Fig. 4b for the sample annealed at 250 °C. The monotonous decrease in resistivity down to 87 K indicates metallic behavior [59]. We observed higher resistivity values than our previous report [58] which we attribute to lower carrier concentrations in our present work. The resistivity values shown in Fig. 4b (2.8  $\text{m}\Omega\cdot\text{cm}$  at room temperature and 2.67  $\text{m}\Omega\cdot\text{cm}$  at 95 K) are comparable to values reported on MBE and PLD films [9,34]. Unlike our

Table 2

Variation of Bulk Resistivity ( $\rho$ ), Hall coefficient ( $R_H$ ), and mobility ( $\mu$ ) with annealing temperature ( $T_{AN}$ ). All samples show *n*-type behavior and measurements are performed at room temperature.

$T_{AN}$ (°C)	$\rho$ ( $\Omega\cdot\text{cm}$ )	$R_H$ ( $\Omega\cdot\text{cm}^3/\text{V}\cdot\text{s}$ )	$\mu$ ( $\text{cm}^2/\text{V}\cdot\text{s}$ )
180	0.0044	0.416	$91.55 \pm 4.82$
220	0.0041	0.465	$111.80 \pm 5.92$
250	0.0024	0.271	$109.61 \pm 3.77$
300	0.0017	0.157	$88.94 \pm 5.78$
350	0.0011	0.057	$50.55 \pm 2.69$

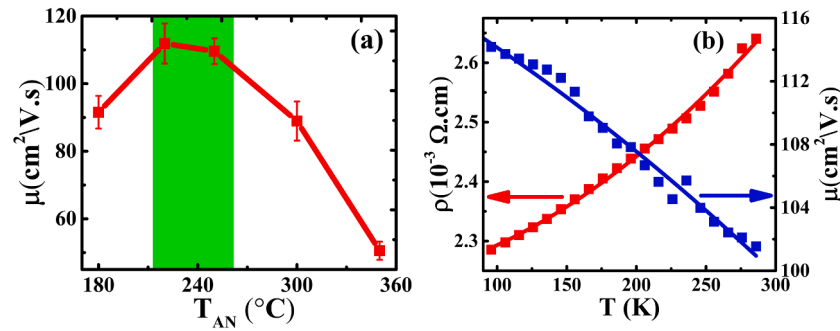


Fig. 4. (a) Variation of mobility as a function of the post-deposition annealing temperature. The shaded region highlights the temperature range with the highest mobility values. (b) Temperature dependence of resistivity and mobility for the sample annealed at 250 °C.

previous result [58], where we observed a strong linear behavior, the present temperature dependence shows a weak non-linear behavior. Similarly, the mobility increased by a modest amount from 101 cm<sup>2</sup>/V.s at room temperature to 115 cm<sup>2</sup>/V.s at 95 K. This increase is controlled by the resistivity variation as the Hall coefficient, R<sub>H</sub>, does not show any specific trend (data not shown).

A few comments are necessary to clarify what may appear as contradictory. First, it may seem conflicting that nominally Se-rich samples - as inferred from EDX data - also show Se vacancies as indicated by the metallic transport behavior. We attribute this to the formation of local Se vacancies in an overall Se-rich sample. Our data is consistent with Analytis *et al.* [45] who report metallic behavior on Se-rich, single-crystal Bi<sub>2</sub>Se<sub>3</sub> with low carrier concentration (~3 × 10<sup>17</sup> cm<sup>-3</sup>). Urazhdin *et al.* [44] showed that both Bi and Se-rich samples behave as n-type materials. Our data implies that local vacancies are significant above 180 °C, but samples remain overall Se-rich to up to 300 °C. We can only speculate at this point as to where the excess Selenium atoms are located as Bi<sub>2</sub>Se<sub>3</sub> starts to crystallize. Xue *et al.* pointed out that Se antisite defects are more stable in Bi-poor/Se-rich samples [43]. It is also likely that the excess Se occupy interstitial sites.

Our data might also appear to contradict the results of Kumar *et al.* [55] who reported Selenium vacancies in as-grown films. The apparent contradiction is explained by noting the different growth temperatures

used in our work (room temperature) and Kumar *et al.* (425 °C). We observed Se rich films when grown at room temperature, whereas Kumar *et al.* report Se deficient films when grown at 425 °C. Their data is consistent with our 350 °C vacuum-annealed sample which is significantly Se deficient (Table 1).

To conclude, we have compared our best data (250 °C annealed sample) with several reports available on 6-10 nm Bi<sub>2</sub>Se<sub>3</sub> deposited on a variety of substrates in Table 3. At the ultrathin level, the literature is dominated by MBE films. Data from exfoliated Bi<sub>2</sub>Se<sub>3</sub> is also included for comparison. Compared to the data on standard substrates such as Al<sub>2</sub>O<sub>3</sub>, Si, Si/SiO<sub>2</sub> at 300 K - and several reports at low temperatures - we note that our sputtered Bi<sub>2</sub>Se<sub>3</sub> films grown on amorphous quartz have similar or lower carrier concentration and higher mobility values. On the other hand, there are several reports on MBE films with significantly lower carrier concentration and higher mobility values than our data [20,26,49,60,24]. In those reports, the films are grown either on lattice-matched or buffered substrates, and/or the authors employed high-field Hall measurements at low temperatures to extract the surface, bulk or impurity band mobilities using a multi-channel model. Such techniques are beyond the scope of this work. The reported surface mobility values are typically an order of magnitude higher than the bulk/impurity band mobilities, as indicated in Table 3. In our case, the Hall measurements are sensitive to bulk properties only. When bulk

Table 3

Reported carrier concentration and mobility values obtained by several groups on ultra-thin Bi<sub>2</sub>Se<sub>3</sub> films. Abbreviations used: FET= Field-effect-transistor, HFH= High-field Hall, QWS=Quantum Well state, TS= Terahertz Spectroscopy.

Thickness (nm)	Growth Method	Subst.	n <sub>2D</sub> (cm <sup>-2</sup> )	n <sub>3D</sub> (cm <sup>-3</sup> )	μ (cm <sup>2</sup> /V.s)	T (K)	Meas. Method
7	<b>This work</b>	<b>Quartz</b>	<b>1.07 × 10<sup>13</sup></b>	<b>1.34 × 10<sup>19</sup></b>	<b>112</b>	<b>300</b>	<b>Bulk, Hall</b>
20	Sputt. [35]	Si/SiO <sub>2</sub>	-	(4-8) × 10 <sup>20</sup>	7-10	-	Bulk, Hall
9-54	Sputt. [36]	Al <sub>2</sub> O <sub>3</sub>	~ 10 <sup>15</sup>	-	0.26-8.8	-	Bulk, Hall
6	Sputt. [58]	Quartz	9.7 × 10 <sup>13</sup>	1.63 × 10 <sup>20</sup>	25	300	Bulk, Hall
6	PLD [32]	InP	1.3 × 10 <sup>14</sup>	-	10	-	Bulk, FET
6	PLD [34]	SrTiO <sub>3</sub>	-	2.6 × 10 <sup>19</sup>	86	2	Bulk, FET
8	MBE [8]	Al <sub>2</sub> O <sub>3</sub>	9.4 × 10 <sup>13</sup>	-	-	300	Bulk, Hall
8	MBE [9]	Al <sub>2</sub> O <sub>3</sub>	3.8 × 10 <sup>13</sup>	-	-	300	Bulk, Hall
6	MBE [19]	Al <sub>2</sub> O <sub>3</sub>	3.5 × 10 <sup>13</sup>	-	350	2	Bulk, Hall
8	MBE [20]	Al <sub>2</sub> O <sub>3</sub>	~3 × 10 <sup>13</sup>	-	~500	2	Surface, HFH
8	MBE [20]	Al <sub>2</sub> O <sub>3</sub>	8 × 10 <sup>12</sup>	-	~3000	2	Surface, HFH
5	MBE [22]	SiO <sub>2</sub>	-	~3 × 10 <sup>20</sup>	~5	300	Bulk, FET
10	MBE [24]	CdS	1.3 × 10 <sup>13</sup>	-	~380	2	Impurity, HFH
10	MBE [24]	CdS	0.04 × 10 <sup>13</sup>	-	~5000	2	Surface, HFH
10	MBE [26]	Al <sub>2</sub> O <sub>3</sub>	3.8 × 10 <sup>13</sup>	-	540	1.5	Bulk, HFH
10	MBE [26]	Al <sub>2</sub> O <sub>3</sub>	0.26 × 10 <sup>13</sup>	-	1550	-	Surface, HFH
7-8	MBE [48]	Si	~3.8 × 10 <sup>13</sup>	~5 × 10 <sup>19</sup>	~100-200	1.5	Bulk, HFH
5-50	MBE [49]	Sub <sup>1</sup>	(1-3) × 10 <sup>12</sup>	-	16,000	1.5	Surface, Hall
7	MBE [50]	SiO <sub>2</sub> /Si	2.2 × 10 <sup>13</sup>	-	-	-	Bulk, FET
2-40	MBE [52]	Al <sub>2</sub> O <sub>3</sub>	-	(0.5-3.5) × 10 <sup>19</sup>	-	-	Bulk, Hall
8	MBE [60]	Si	8.4 × 10 <sup>10</sup>	-	2880	-	Surface, TS
8	MBE [60]	Si	4.5 × 10 <sup>12</sup>	-	54	-	QWS, TS
10	VWE [61]	Mica	~6.6 × 10 <sup>13</sup>	-	~100	300	Bulk, HFH
10	Exfoliated [62]	SiO <sub>2</sub> /Si	~1 × 10 <sup>13</sup>	-	320 -1500	2	Bulk, FET

<sup>1</sup>Al<sub>2</sub>O<sub>3</sub>/In<sub>2</sub>Se<sub>3</sub>/(Bi<sub>0.5</sub>In<sub>0.8</sub>)<sub>2</sub>Se<sub>3</sub>

properties are reported, our data compares very well with MBE work [48]. In our previous work, we have noted that ultra-thin films grown on Al<sub>2</sub>O<sub>3</sub> substrates show higher mobility [58]. Taken together, we believe that the sputtered films will also show significant improvements if grown on lattice-matched substrates and such work will be undertaken in the future.

#### 4. Conclusions

Transport properties of sub-10 nm Bi<sub>2</sub>Se<sub>3</sub> thin-films fabricated using magnetron sputtering are investigated as a function of the post-deposition vacuum annealing temperature. As-deposited samples – grown at room temperature – are Se-rich due to a disproportionately high rate of Selenium sputtering from the stoichiometric target. We observed the highest carrier concentration and mobility values when Se-rich samples are annealed at lower temperatures. The lowest carrier concentration ( $\sim 1 \times 10^{19} \text{ cm}^{-3}$ ) and highest mobility (over 100 cm<sup>2</sup>/V.s) are obtained in the 220-250 °C annealing temperature range. The crystal structure, on the other hand, is best at 300 °C when bismuth and selenium is in the stoichiometric 2:3 ratio. Overall, our work demonstrates that the transport properties of Bi<sub>2</sub>Se<sub>3</sub> films are highly tunable through post-annealing temperature, which may be of interest in applied areas such as nanoelectronics and spintronics.

#### Declaration of Competing Interest

The authors declare that they have no known competing financial interests or personal relationships that could have appeared to influence the work reported in this paper.

#### Acknowledgements

The authors would like to acknowledge NSF CAREER grant (ECCS, Award # 1846829) for partial support of this work.

#### Credit Author Statement

The author names provided in the submission are correct and not changes to the authorship have been made.

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